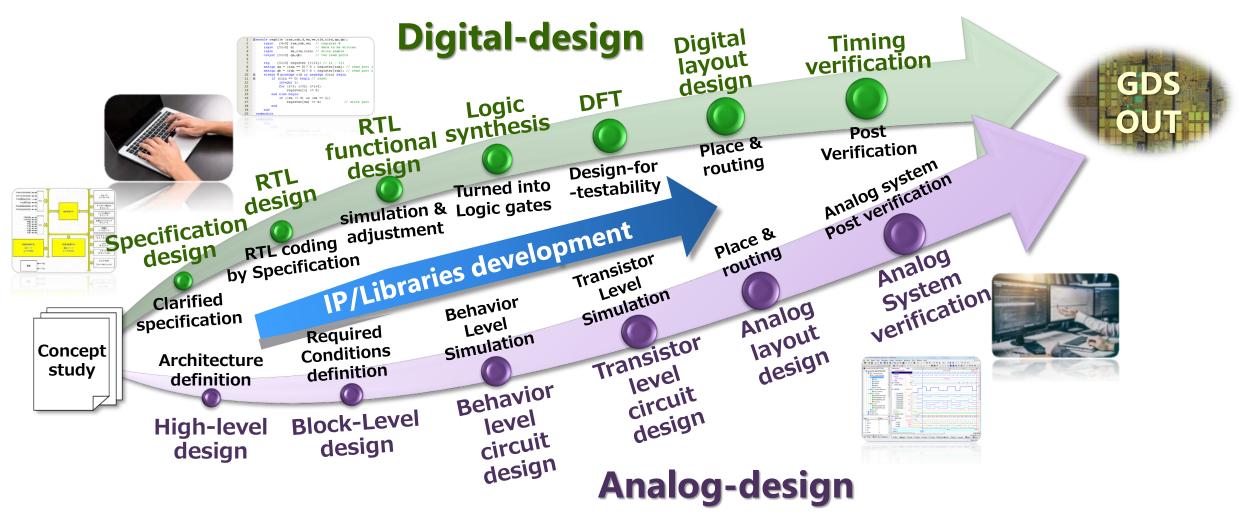
Design Services -LSI design development flow-

LSI design and IP/libraries development

►LSI design development flow



Design development (LSI products)

LSI design and IP/libraries development

- **▶** Digital design engineering
 - igoplusProposals for design development flows and design engineering of LSI products Entire processes from specifications design to implementation are supported by abundant LSI development expertise cultivated as IDM(*1) .
 - Latest design method and know-how contribute to designs development with short TAT.

♦LSI development results

Development results

Abundant development results by fabrication, advanced process, VLSI, and high frequency

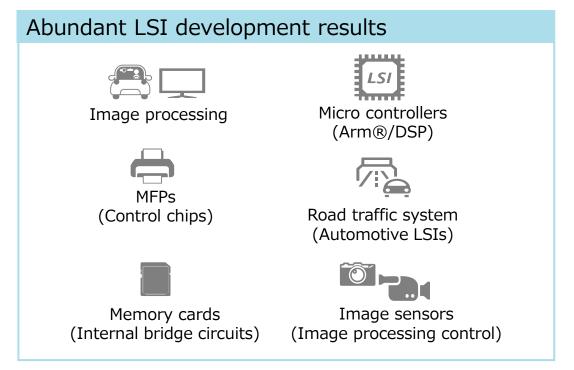
•Fabrication SEC^(*2),TSMC^(*3), UMC^(*4), GF^(*5),

and JSC(*6)

•Process To 5 nm

•Scale 150 Mgates

•Frequency 1GHz



^(*1) IDM: Integrated Device Manufacturer (*2) SEC: Samsung Electronics Co., Ltd.

^(*3) TSMC: Taiwan Semiconductor Manufacturing Company Limited (*4) UMC: United Microelectronics Corporation (*5) GF: GlobalFoundries (*6) JSC: Japan Semiconductor Corporation

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^{*} All other company names, product names, and service names may be trademarks of their respective companies.

Design development (LSI products)

► Analog design engineering

♦Libraries development results

Process support results

Abundant results by fabrication and advanced process

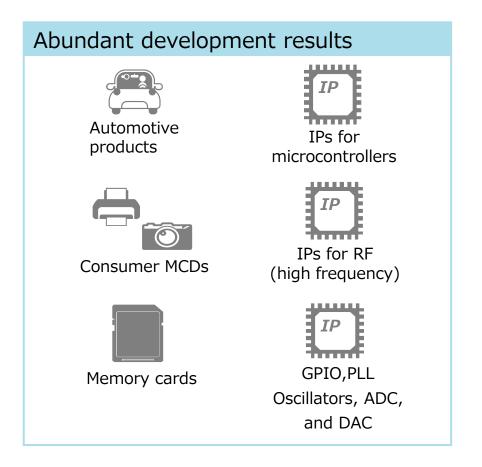
•Fabrication $SEC^{(*1)}$, $TSMC^{(*2)}$, $UMC^{(*3)}$,

and JSC^(*4) (CMOS, CD, BiCD)

•Process To 16nm

·Related products SoC, ASIC, linear sensors,

area sensors



^(*1) SEC: Samsung Electronics Co., Ltd. (*2) TSMC: Taiwan Semiconductor Manufacturing Company Limited

^(*3) UMC: United Microelectronics Corporation (*4) JSC: Japan Semiconductor Corporation

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